



**Serial No. 10/621,253**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Sudhakar Kale et al.

Examiner: Leigh Garbowski

Serial No.: 10/621,253

Group Art Unit: 2825

Filed: July 14, 2003

Docket No.: 884.142US2

Title: STRUCTURAL REGULARITY EXTRACTION AND FLOORPLANNING IN  
DATAPATH CIRCUITS USING VECTORS

Assignee: Intel Corporation

Customer Number: 21186

**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

This responds to the Office Action mailed on July 14, 2006. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a three-month extension of the period for responding to the Office action, thereby moving the deadline for response from October 14, 2006 to January 14, 2007 (which was during Federal Holiday Weekend).